

Summary of Lesson Plans of College Faculty

Name of College: Adarsh Mohila Mahavidyalaya (Bhisani), Academic Session: 2017-18 Semester: Odd/Even For the month of July-Dec, 2017

S No.	Name of Assistant / Associate Professor	Subject	Topics / Chapters to be covered	Academic activity to be organized	Topic of Assignments / Tests to be given to the students
	<u>Nutan Sharma</u>	<u>Logical organization of computers-I</u>	<u>July - Number System, BCD codes.</u>		
			<u>Aug - ASCII, EBCDIC, Unicode, Boolean Algebra Truth table</u>	<u>Presentation on BCD codes</u>	<u>Test - Number system.</u>
			<u>Sep - Boolean Functions Venn Diagram, K-Maps. Introduction - Digital signal</u>		<u>Test - Error detection correcting codes ASCII EBCDIC, Unicode.</u>
			<u>Oct - Basic Gates - AND, OR, NOT, Universal gates - NAND, NOR, other</u>	<u>Presentation on Boolean function - Venn Diagram, Kmap.</u>	<u>Test - Boolean Functions.</u>
			<u>Grates - XOR, XNOR etc: Combinational logic</u>		<u>Assignment on Basic gates and Universal gates</u>
			<u>Nov - Combinational Circuits - Half adder, full adder, BCD to seven-segment decoder.</u>	<u>Presentation. Basic gates and universal gates</u>	<u>Test - Combinational logic</u>
			<u>Dec - Revision</u>		

Nutan
25/10/17

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			<u>Sep - Boolean Functions Venn Diagram, K-Maps. Introduction - digital signal</u>		<u>Test - Error detection correcting codes ASCII EBCDIC, Unicode.</u>
			<u>Oct - Basic gates - AND, OR, NOT, Universal gates - NAND, NOR, other gates - XOR, XNOR etc.</u>	<u>Presentation on Boolean function - Venn Diagram, Kmap.</u>	<u>Test - Boolean Functions.</u>
			<u>Combination logic</u>		<u>Assignment on Basic gates and Universal gates</u>
			<u>Nov - Combinational Circuits - Half adder, full adder, BCD to seven-segment decoder.</u>	<u>Presentation. Basic gates and universal gates</u>	<u>Test - Combinational logic</u>
			<u>Dec - Revision</u>		

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	Srinivasi Sharma	Computer Graphics	<p>July - Introduction to Computer graphics, Basics</p> <p>Aug - Application areas of Computer graphics, subroutines of graphic system</p> <p>Aug - Random Scan, Raster Scan, Input Devices</p> <p>Sep - Points, line drawing algorithms, ellipse Algo, Redundant Fill, Flood Fill</p> <p>Oct - 2D Geometrical Transformations, 2D viewing</p> <p>Oct - 3D object representation, Polygon surfaces, quadratic surfaces</p> <p>Oct - 3D Geometric Transformations, translation, Rotation, Scaling, Shearing</p> <p>Nov - 3D Viewing, Viewing Coordinate, projection</p> <p>Dec - Raster Scan</p>	<p>Presentation on Video Display Devices</p>	<p>Test - line algorithms</p> <p>Assignment - Graphics primitives</p> <p>Test - Bresenham Algo, ellipse Algo</p> <p>Test - 2D Geometrical Transformations</p>

Srinivasi
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(Bhimani)

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	Nutan Sharma	Logical organization of computer-I	July - Number system, BCD codes. Aug - ASCII, EBCDIC, VH code, Boolean Algebra Truth table Sep - Boolean functions Venn diagram, K-Maps. Introduction - Digital signal Oct - Basic gates - AND, OR, NOT, Universal gates - NAND, NOR, other gates - XOR, XNOR etc. Combinational logic Nov - Combinational Circuits - Half adder, full adder, BCD to Seven-Segment Decoder	Representation on BCD codes Presentation on Boolean function - Venn Diagram, Kmap. Presentation: Basic gates and universal gates	Test - Number system. Test - Error detection correcting codes, ASCII, EBCDIC, BCD codes. Test - Boolean Functions Assignment on Basic gates and universal gates Test - Combinational logic
			Dec - Revision		

Nutan
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